

REMARKS

Summary of the Office Action

Claim 1-3, 7, 10, 12, 13, and 19 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Kubota, et al. (US 2002/0171792) in view of Mitsui et al. (US 5,408,345), in view of Maeda et al. (US 7,123,325), and further in view of official notice/admitted prior art.

Claims 4, 5, 14, 15, and 21 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Kubota et al., Mitsui et al., Maeda et al., official notice/admitted prior art, and further in view of and further in view of You (US 7,023,508).

Claims 8, 9, 11, 17, 18, and 20 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Kubota et al., Mitsui et al., Maeda et al., and official notice/admitted prior art.

Summary of the Response to the Office Action

Applicants have canceled claims 6-7 and 16 without prejudice or disclaimer and amended claims 1, 8, 10, 12, 19, and 21, to further define the invention. Accordingly, claims 1-5, 8-15, and 17-21 remain pending for further consideration.

All Claims Comply with 35 U.S.C. §103(a)

In the Office Action, the Examiner rejected claims 1-3, 7, 10, 12, 13 and 19 under 35 U.S.C. § 103(a) as being unpatentable over Kubota et al. in view of Mitsui et al., in view of Maeda et al., and further in view of Official Notice/ARA (Applicant's Related Art); and rejected claims 4, 5, 14, 15 and 21 under 35 U.S.C. § 103(a) as being unpatentable over Kubota et al., Mitsui et al., Maeda et al..
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al., and Official Notice/ARA as applied above, and further in view of You; and rejected claims 8, 9, 11, 17, 18 and 20 under 35 U.S.C. § 103(a) as being unpatentable over Kubota et al., Mitsui et al., Maeda et al., and Official Notice/ARA as applied above, and further in view of Official Notice.

These rejections are respectfully traversed and reconsideration is requested. Claim 1 is allowable over the cited references in that claim 1 recites, for example, a combination of elements, “...a thin film transistor connected to the gate line and the data line and including a gate electrode, an active layer, and source and drain electrodes...a reflective layer on the second organic material layer having a transmissive hole at the open portion, the reflective layer substantially not overlapping the thin film transistor...” Claim 10 is allowable over the cited references in that claim 10 recites, for example, a combination of elements, “...a thin film transistor connected to the gate line and the data line and including a gate electrode, an active layer, and source and drain electrodes...a reflective layer on the second organic material layer having a transmissive hole corresponding to the open portion, the reflective layer substantially not overlapping the thin film transistor...” Claim 12 is allowable over the cited references in that claim 12 recites, for example, a combination of elements, “...forming a thin film transistor connected to the gate line and the data line and including a gate electrode, an active layer, and source and drain electrodes...forming a reflective layer on the second photosensitive organic material layer having a transmissive hole corresponding to the open portion, the reflective layer substantially not overlapping the thin film transistor...” Claim 19 is allowable over the cited references in that claim 19 recites, for example, a combination of elements, “...forming a thin film transistor connected to the gate line and the data line and including a gate electrode, an active layer, and source and drain electrodes...forming a reflective layer on the second organic material layer having a transmissive hole corresponding to the

open portion, the reflective layer substantially not overlapping the thin film transistor..." Claim 21 is allowable over the cited references in that claim 21 recites, for example, a combination of elements, "...a thin film transistor connected to the gate line and the data line and including a gate electrode, an active layer, and source and drain electrodes... a reflective layer on the second organic material layer having a transmissive hole at the open portion, the reflective layer substantially not overlapping the thin film transistor..." None of the cited references teaches or suggests at least this feature of the claimed invention.

The Examiner alleges on pages 2 and 4 of the Office Action, inter alia, that Mitsui et al. teaches the reflective layer substantially not overlapping the thin film transistor of the claimed invention. However, Applicants disagree with the Examiner. For example, Mitsui et al. explicitly teaches the reflection electrode 38 overlapping and contacting the drain electrode of the thin film transistor through the contact hole 43. See, for example, FIG. 5 and column 9 line 68 through column 10 line 2. That is, the reflection electrode 38 of Mitsui et al. overlaps the thin film transistor. Accordingly, Mitsui et al. never teaches the reflection layer substantially overlapping the thin film transistor of the claimed invention.

For at least the reasons set forth above, Applicants respectfully submit that claim 1 and its dependent claims 2-5 and 8-9, claim 10 and its dependent claim 11, claim 12 and its dependent claims 13-15 and 17-18, claim 19 and its dependent claim 20, and claim 21 are allowable over the cited references.

Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

CONCLUSION

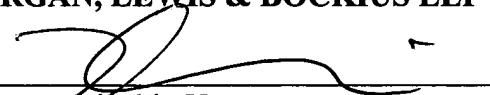
In view of the foregoing, Applicants respectfully request reconsideration and the timely allowance of the pending claims. Should the Examiner feel that there are any issues outstanding after consideration of the response, the Examiner is invited to contact the Applicants' undersigned representative to expedite prosecution.

If there are any other fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. §1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

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